

AMENDMENTS TO THE CLAIMS

1. (Canceled).
2. (Previously Presented) The method of claim 3 further wherein transferring comprises transferring a number of bytes specified by an operand from a memory.
3. (Currently Amended) A method comprising
configuring a cache memory of a processor to operate ~~as a random access~~
memory in a mode in which cache lines are not replaced;
transferring an authenticated code module to the cache memory of the processor,
authenticating the authentic code module storing in the cache memory, and
executing the authenticated code module from the cache memory ~~operating as a~~
~~random access memory~~ in response to determining that the authenticated code module
stored in the cache memory is authentic.
4. (Original) The method of claim 3 further comprising invalidating the cache
memory prior to storing the authenticated code module in the cache memory.
5. (Canceled).
6. (Previously Presented) The method of claim 3 further comprising determining
whether the authenticated code is authentic based upon a digital signature of the
authenticated code module.

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7. (Previously Presented) The method of claim 3 further comprising obtaining a first value from the authenticated code module stored in the cache memory;
- computing a second value from the authenticated code module; and
- determining that the authenticated code module is authentic in response to the first value and the second value having a predetermined relationship.
8. (Previously Presented) The method of claim 3 further comprising retrieving a key,
- decrypting a digital signature of the authenticated code module with the key to obtain a first value,
- hashing the authenticated code module to obtain a second value; and
- executing the authenticated code module in response to the first value and the second value having a predetermined relationship.
9. (Original) The method of claim 8 wherein
- decrypting comprises using the key to RSA-decrypt the digital signature, and
- hashing comprises apply a SHA-1 hash to the authenticated code module to obtain the second value.
10. (Original) The method of claim 8 further comprising retrieving the key from the processor.

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11. (Original) The method of claim 8 further comprising retrieving the key from a chipset.

12. (Previously Presented) The method of claim 8 further comprising retrieving the key from a token.

13. (Previously Presented) The method of claim 3 wherein transferring comprises receiving the authenticated code module from a machine readable medium.

14. (Canceled).

15. (Currently Amended) A computing device, comprising
~~a chipset;~~

~~a memory coupled to the chipset;~~

a memory controller coupled to the memory;

a machine readable medium interface to receive an authenticated code module from a machine readable medium;

~~a private memory coupled to the chipset;~~

a separate private memory controller coupled to the private memory; and

a processor to transfer the authenticated code module from the machine readable medium interface to the private memory and to authenticate the authenticated code module stored in the private memory,

~~wherein the chipset comprises a memory controller coupled to the memory and a separate private memory controller coupled to the private memory.~~

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16. (Currently Amended) The computing device of claim 15, ~~wherein further~~
~~comprising~~

~~the chipset comprises a key, and wherein~~

the processor authenticates the authenticated code module stored in the private
memory based upon the key ~~of the chipset~~.

17. (Previously Presented) The computing device of claim 15, wherein
the processor comprises a key and authenticates the authenticated code module
stored in the private memory based upon the key of the processor.

18. (Currently Amended) The computing device of claim 14, further comprising
a token ~~coupled to the chipset~~, the token comprising a key, wherein
the processor authenticates the authenticated code module stored in the private
memory based upon the key of the token.

19-21. (Canceled).